

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New Patent Application of)
Hidekazu NOGUCHI)
Japanese Priority Application No. 2003-173003) Attn: Applications
Japanese Priority Date: June 18, 2003) Branch
For: SEMICONDUCTOR INTEGRATED)
CIRCUIT) Date: February 6, 2004

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please preliminarily amend the subject application as follows:

06/03/2004 LWISE1 00000003 192880 10772390 01
01 FC:1203 290.00 DA